

Notice of Allowability

Application No.

09/589,716

Examiner

Chat C. Do

Applicant(s)

KOSONOCKY, STEPHEN V.

Art Unit

2193

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 08/31/2005.
2. ☒ The allowed claim(s) is/are 1,3-6 and 13.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
- * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

REASONS FOR ALLOWANCE

1. Claims 1, 3-6, and 13 are allowed.
2. Claims 2, 7-12, and 14-20 are canceled.
3. The following is an examiner's statement of reasons for allowance:

The prior art of records fails to disclose or render an obviousness of an apparatus for summing at least two binary values comprising: a binary adder circuit having dynamic logic without inversion of signals driving one or more dynamic nodes associated logic for implementing an exclusive OR function that generates the binary output value without one of a positive and a negative complementary version of the carry value wherein the $S(n)$ is implemented according as $\wedge(p(n) * C(n-1))(p(n) + C(n-1))$ as seen in independent claim 1. In addition, the prior art of records fails to disclose or render an obviousness of a dynamic N-bit parallel adder comprising: first, second, third, and fourth logic stage, wherein at least one of the logic stages has dynamic logic without inversion of signals driving one or more dynamic nodes associated with the dynamic logic; and the fourth logic stage implements an XOR function to generate the summation signal and bit I of the summation signal as $\wedge[p_i G_{i-1} + p_i P_{i-1} G_{i-2} + p_i P_{i-1} P_{i-2} G_{i-3}][G_{i-1} + P_{i-1} G_{i-2} + P_{i-1} P_{i-2} G_{i-3} + p_i]$ as seen in independent claims 6 and 13.

The closest found prior arts are Suzuki (U.S. 3,646,332), Lee (U.S. 5,905,667), and Jiang et al. (U.S. 5,943,251). All three references disclose a dynamic binary adder.

However, neither individual nor in combination of Suzuki, Lee, and Jiang et al. disclose a binary adder circuit having dynamic logic without inversion of signals driving one or more dynamic nodes associated logic for implementing an exclusive OR function that generates the binary output value without one of a positive and a negative complementary version of the carry value wherein the $S(n)$ is implemented according as $\wedge(p(n) * C(n-1)) * (p(n) + C(n-1))$ and the fourth logic stage implements an XOR function to generate the summation signal and bit I of the summation signal as $\wedge[p_i G_{i-1} + p_i P_{i-1} G_{i-2} + p_i P_{i-1} P_{i-2} G_{i-3}] * [G_{i-1} + P_{i-1} G_{i-2} + P_{i-1} P_{i-2} G_{i-3} + p_i]$ as seen above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chat C. Do whose telephone number is (571) 272-3721. The examiner can normally be reached on 7:00AM to 5:00PM M-Th.


If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chaki Kakali can be reached on (571) 272-3719. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2193

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Chat C Do
Examiner
Art Unit 2193

October 27, 2005


KAKALI CHAKI
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100